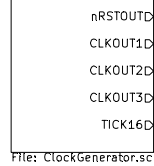
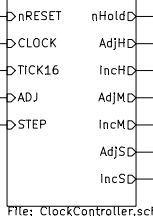


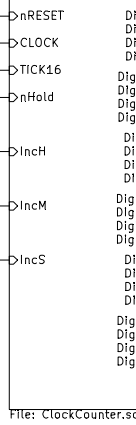
Sheet: ClockGenerator



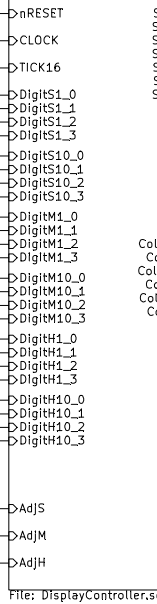
Sheet: ClockController



Sheet: ClockCounter



Sheet: DisplayController



R103 470x4



R104 470x4



U101 ULN2003LV



LED101A C-5525SRD



LED101B C-5525SRD



LED102A C-5525SRD



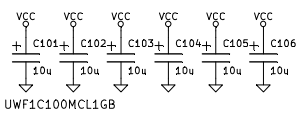
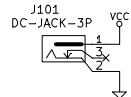
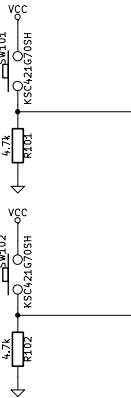
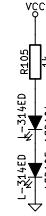
LED102B C-5525SRD

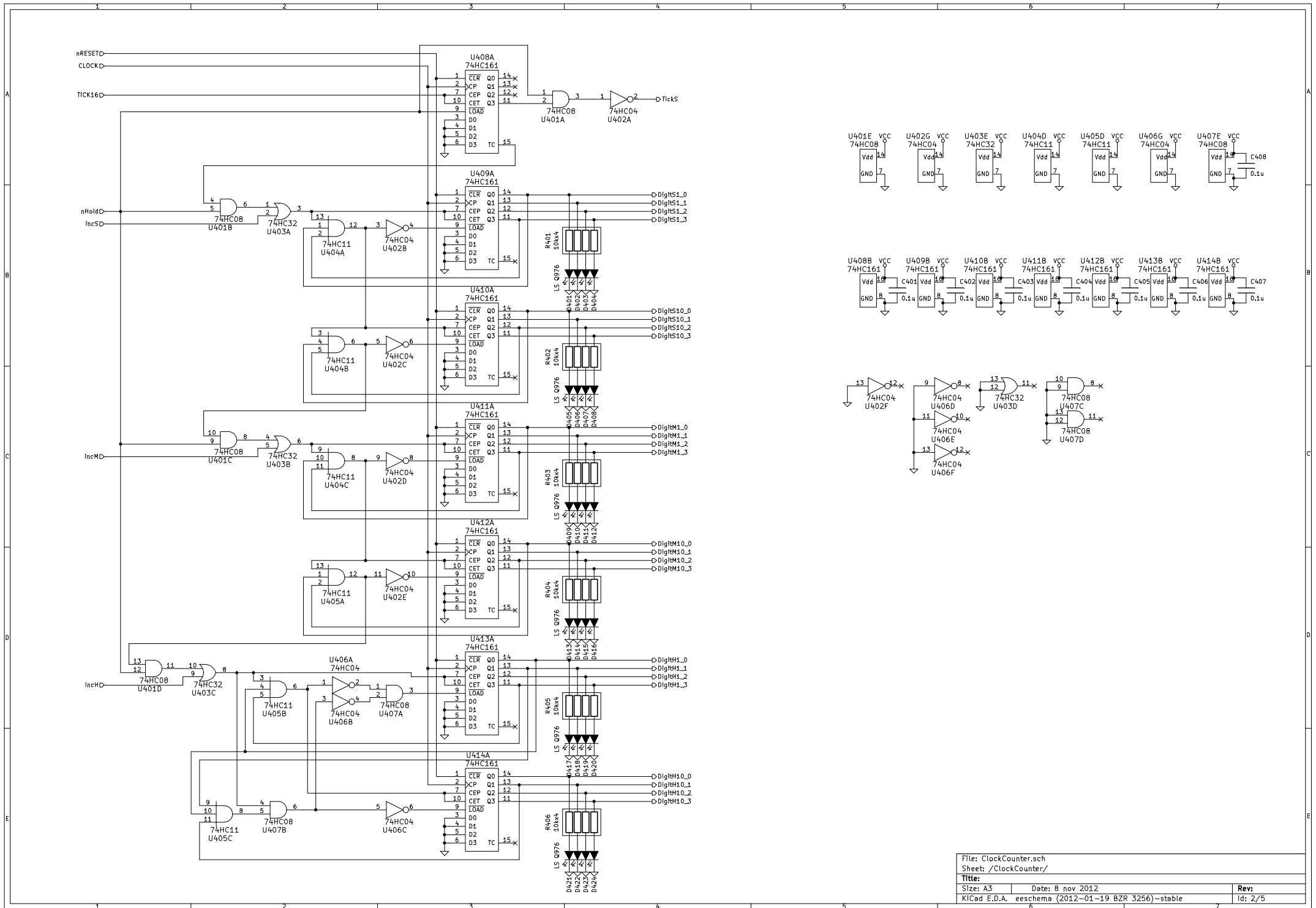


LED103A C-5525SRD

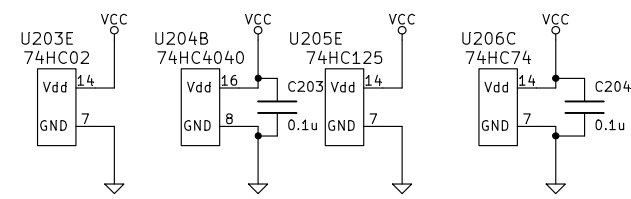
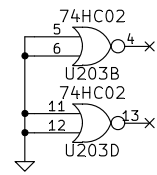
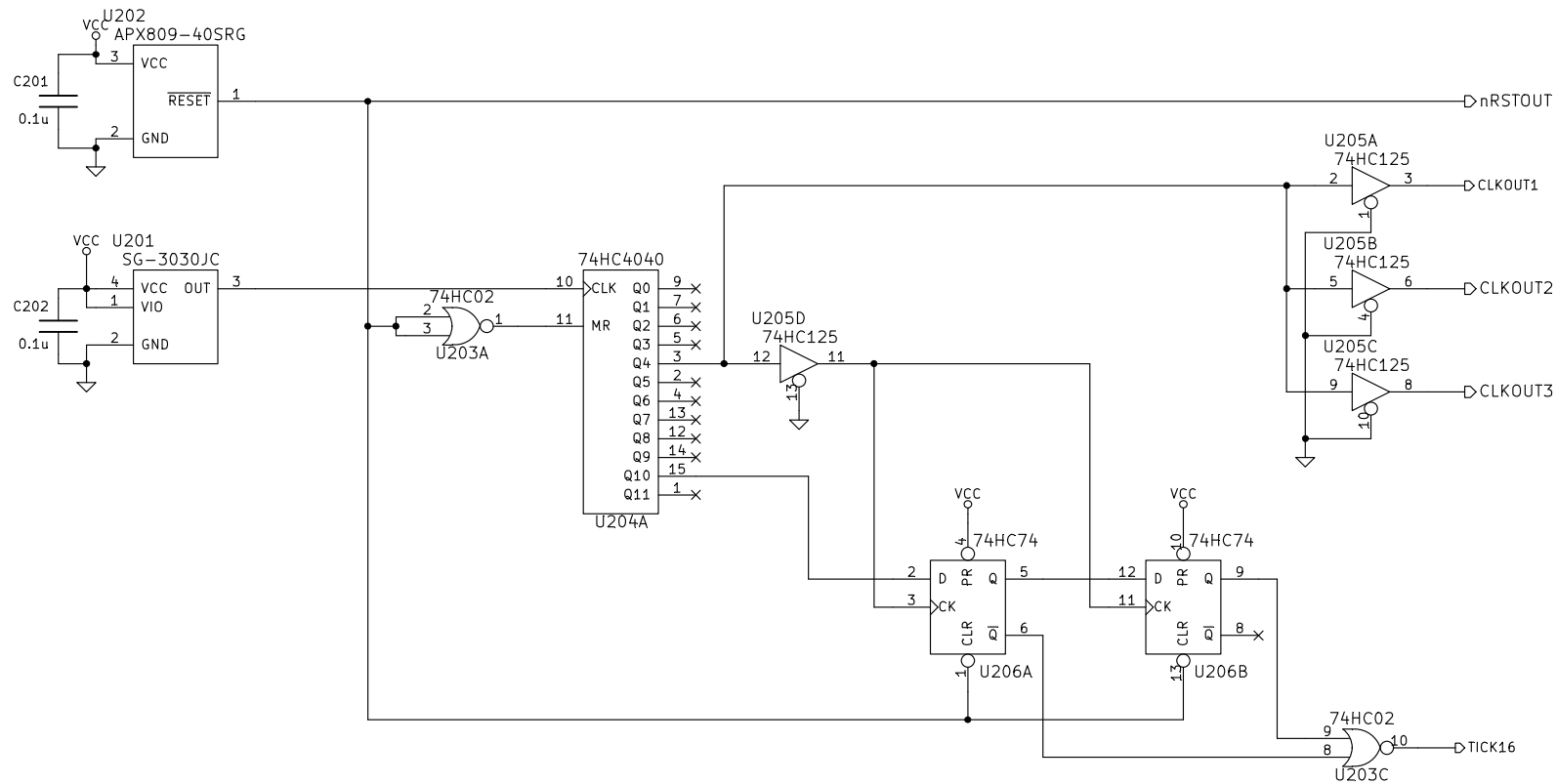


LED103B C-5525SRD

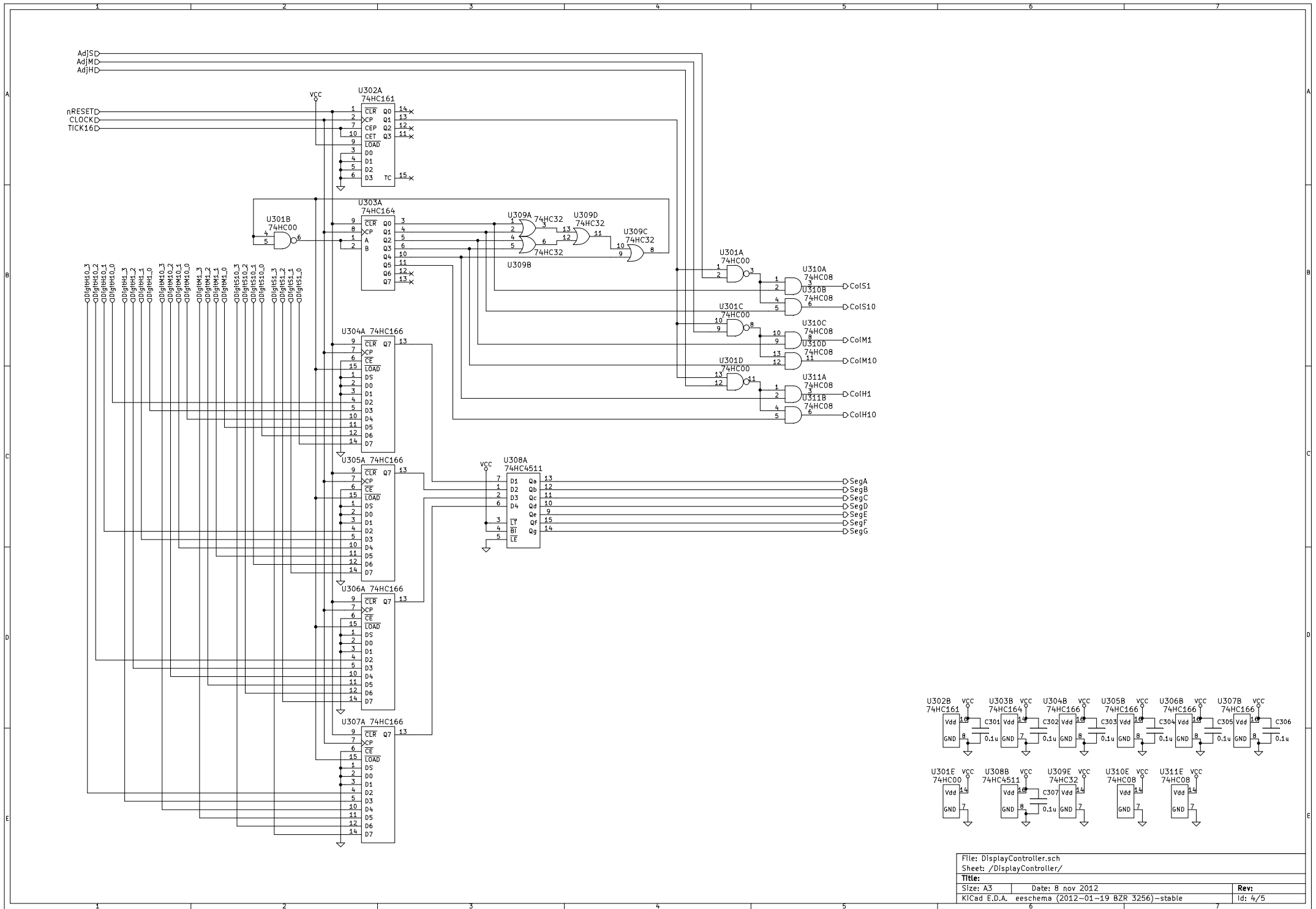


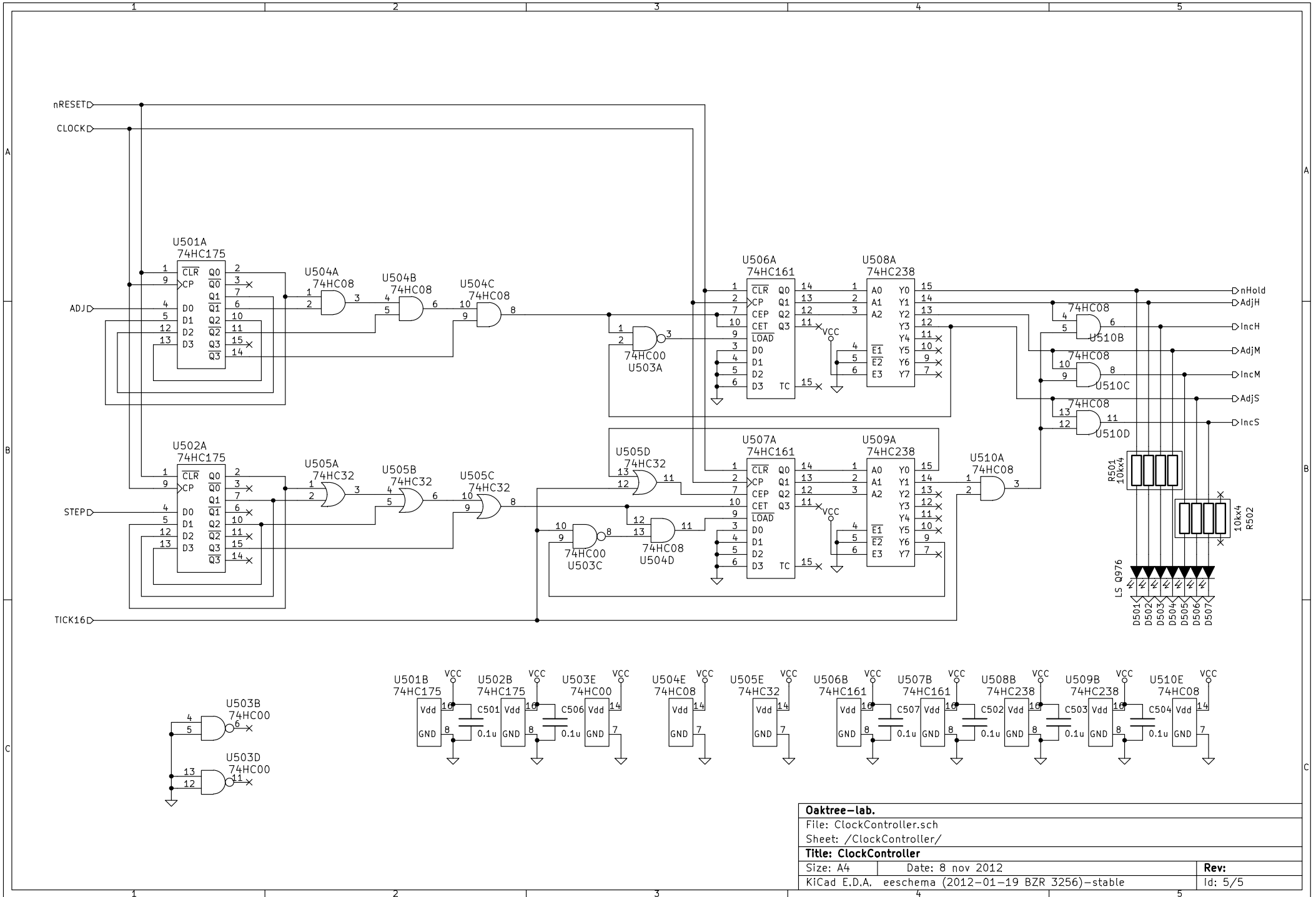


File: ClockCounter.sch
 Sheet: /ClockCounter/
 Title:
 Size: A3 Date: 8 nov 2012
 KICad E.D.A. eeschema (2012-01-19 BZR 3256)-stable Rev: 2/5



Oaktree-lab.		
File: ClockGenerator.sch		
Sheet: /ClockGenerator/		
Title: ClockGenerator		
Size: A4	Date: 8 nov 2012	Rev:
KiCad E.D.A. eeschema (2012-01-19 BZR 3256)-stable		Id: 3/5





Oaktree-lab.	
File: ClockController.sch	
Sheet: /ClockController/	
Title: ClockController	
Size: A4	Date: 8 nov 2012
KiCad E.D.A. eschema (2012-01-19 BZR 3256)-stable	
Rev:	
Id: 5/5	